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XILINX, INC

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10/25/2006 RMEBRAH1 00000065 240040 10716947

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(Depositor's munic Pat Tompkins (Signature) Dynns (Date) 2006

ſ	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
Ę	10/716 947	11/18/2003	Jay T. Young	X-1386 US	6459

TITLE OF INVENTION: METHODS OF ROUTING PROGRAMMABLE LOGIC DEVICES TO MINIMIZE PROGRAMMING TIME

APPLN, TYPE	SMALL ENTITY	ISSUE FEE DUF	PUBLICATION FEE DUB	PREV, PAID ISSUE FEE	TOTAL FRE(S) DUE	DATE DUB			
nonprovisional	NO	\$1400	50	\$0	\$1400	11/16/2006			
EXAMINER DOAN, NGHIA M		ART UNIT	CLASS-SUBCLASS]					
		2825	716-012000						
CFR 1.363). Change of corress Address form PTO/S "Fee Address" int PTO/SB/47; Rev 03- Number is required	dication (or "Fee Address 02 or more recent) attack	nnge of Correspondence s" Indication form hed. Use of a Customer	2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively. (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.						
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Inventor(s):

Jay T. Young et al.

Assignee: XILINX, INC.

Serial No.: 10/716,947

conf.no. 6459

Filed:

November 18, 2003

Title: Methods of Routing Programmable Logic Devices to Minimize Programming Time

Docket No.: X-1386 US

Enclosed: *Return Receipt Postcard

- Fee Transmittal - Part B

Date: October 24, 2006

VIA FACSIMILE 571-273-2885

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